

1 39499/FLC/B600-BP1426

PERFORMANCE INDICATOR FOR A HIGH-SPEED COMMUNICATION SYSTEM

5 CROSS-REFERENCE TO RELATED APPLICATION(S)

This application claims the priority of U.S. Provisional Patent Application No. 60/198,836, filed April 21, 2000, which is incorporated herein by reference.

10 BACKGROUND OF THE INVENTION

This invention relates generally to networked systems and more specifically to performance indicators in a high-speed communication system.

High-speed network devices within high-speed communication systems may employ disparate communication modules with each communication module handling a separate function for the high-speed communications device. Each communication module may generate a variety of status signals related to the communication module's internal processes. These status signals may relate to the quality of the communication link established by the high-speed communications device. For example, a communication module may have an internal process for handling an auto-negotiation process, such as the auto-negotiation process defined in the well known IEEE 802.3 network standard as employed by Broadcom Inc.'s BCM5400 100/1000BASE-T Gigabit Ethernet Transceiver, in which case the communication module may provide status signals indicating the progress of the auto-negotiation process.

A high-speed communications device may comprise a number of communication modules with each module generating its own status signals. The format of the status signals may vary in characteristics dependent on the nature of a variable value encoded within the status signal. For example, some of the signals may be binary in nature, indicating either total failure or normal operation, while other signals may encode quantitative

35

information, such as number of communication errors per unit of time.

5 Each communication module may generate its own status signals indicating that the communication module is functioning normally independently of a communication module that may be experiencing processing errors. In this case, monitoring a single status signal or a set of status signals from a communication module may not indicate the quality of the  
10 communication link established by the high-speed communications device.

Therefore, a need exists for a method for generating a high-speed communication system performance signal incorporating a variety of communication status signals. The present invention  
15 meets such need.

#### SUMMARY OF THE INVENTION

In one aspect of the current invention, an apparatus  
20 comprising a quality indicator logic module receives a set of communication status signals from a transceiver in a high-speed communications network. The quality indicator logic module generates a link quality indicator signal based on the set of communication status signals.

25 In another aspect of the current invention, the link quality indicator signal is used to drive a Light Emitting Diode (LED) creating a visual display.

In another aspect of the invention, a quality indicator process is provided for generating a link quality indicator  
30 signal using an auto-negotiation status signal, a link status signal, a local receiver status signal, a receive error status signal, and a MSE communication status signal.

At reset, the quality indicator process generates a link quality indicator signal at a first signal level.

1 39499/FLC/B600-BP1426

5 The quality indicator process then sets the link quality indicator signal to a second signal level if the auto-negotiation complete status signal indicates a local transceiver auto-negotiation process is complete. If the auto-negotiation process is not complete, the quality indicator process sets the link quality indicator signal to the first signal level and continues processing by checking the auto-negotiation complete status  
10 signal again.

The quality indicator process checks the link status signal and returns to checking the auto-negotiation complete status signal if the link status signal indicates that a network channel has not been established.

15 The quality indicator process checks the local receiver communication status signal and sets the link quality indicator signal at the first signal level and continues checking the link status if a local receiver status signal indicates that the local transceiver is not functional.

20 The quality indicator process then sets the link quality indicator signal at the second signal level if the local receiver status signal indicates that the local transceiver is functional.

The quality indicator process then sets the link quality indicator signal at the first signal level for a first period of  
25 time and then sets the link quality indicator signal at the second signal level for the same period of time if a receive error status signal indicates that the local transceiver has a reception error.

The quality indicator process sets the link quality  
30 indicator signal at the first signal level for a second period of time and sets the link quality indicator signal at the second signal level for the same period of time if the MSE status signal indicates that a MSE of the local transceiver exceeds a SNR threshold status signal level.

35

The quality indicator process repeats the process starting with checking the link status signal.

5

#### BRIEF DESCRIPTION OF THE DRAWINGS

These and other features, aspects, and advantages of the present invention will become better understood with regard to the following description and accompanying drawings wherein:

10 FIG. 1 is a block diagram depicting an embodiment of a performance indicator according to the present invention;

FIG. 2 is a block diagram depicting an LED embodiment of a performance indicator according to the present invention; and

15 FIG. 3 is a process flow diagram of an embodiment of a performance indicator according to the present invention.

#### DETAILED DESCRIPTION OF THE INVENTION

FIG. 1 is a block diagram depicting an embodiment of a performance indicator according to the present invention. A  
20 local network chip 10 is operably coupled through a computer network 30 to a remote network device 20 resulting in a high speed communication system 22. The local network chip and the remote network device communicate with each other over the network using the known IEEE 802.3 networking standard creating  
25 a network channel 32 through the network. A single performance indicator signal, herein termed a link quality indicator signal, 40 is generated by the local network chip to indicate the quality of the network channel. In operation, the local network chip monitors the quality of the network channel and changes the level  
30 of the link quality indicator signal based on the quality of the network channel.

The local network chip includes a local transceiver 50 operably coupled via a plurality of network channel status signals 53 to a quality indicator logic module 60. The quality  
35 indicator logic module includes an encoded process 62 for

receiving the plurality of network channel status signals transmitted by the local transceiver and processing the plurality  
5 of received network channel status signals to generate a single link quality indicator signal. The quality indicator logic module 60 generates a link quality indicator signal including a plurality of possible timing sequences 70. Each of the timing sequences encodes a different aspect of the quality of the  
10 network channel.

In one embodiment of a link quality indicator according to the present invention, aperiodic link quality indicator signals are generated to indicate either that the network link is fully operational or that the network link has completely failed.

15 In another embodiment of a link quality indicator according to the present invention, a plurality of periodic link quality indicator signals are generated with the period of the generated link quality indicator signal indicating a different problem within the local network chip's communication modules.

20 The local transceiver includes a plurality of operably coupled communication modules 52 that generate and control signals used to establish the network channel. The communication modules generate status signals including the plurality of network channel status signals transmitted by the local  
25 transceiver to the quality indicator logic module. These network channel status signals are generated according to the known IEEE 802.3 networking standard. The network channel status signals may or may not share the same format. For example, some of the signals may be binary in nature, indicating either total failure  
30 or normal operation, while other signals may encode quantitative information, such as number of errors per unit of time.

In an embodiment of a local network chip according to the present invention, the local transceiver includes communication modules 52. The communication modules include higher level logic  
35 for controlling the transceiver operations as well as signal

processing circuitry and signal processing logic responsive to the higher level logic (for example, the PHY module) for  
5 controlling the operations of the signal processing circuitry.

The communication modules further include logic for auto-negotiation of a master/slave relationship according to the IEEE 802.3 standard. The communication modules generate an auto-negotiation complete signal 76 (for example, the flp\_link\_good  
10 signal as defined in the IEEE 802.3 standard) indicating that the communication modules have completed an attempted auto-negotiation with the remote network device according to the IEEE 802.3 standard.

Even though the auto-negotiation sequence is completed, the  
15 link may not have been established. The communication modules include logic for generating a link status signal 76 according to the IEEE 802.3 networking standard at the end of the auto-negotiation sequence indicating whether or not a network link has been properly established.

20 The communication modules further include logic for generating a Signal to Noise Ratio (SNR) threshold value signal 78 that correlates with the a maximum Bit Error Rate (BER) as provided for in the IEEE 802.3 networking standard. The SNR threshold is programmable by an external control program (not  
25 shown) thus facilitating the integration of the local network chip in a larger network device.

In an embodiment of a quality indicator according to the present invention, the SNR threshold is set slightly higher than the SNR threshold called for in the IEEE 802.3 networking  
30 standard.

The communication modules generate a local receiver status signal 80 as provided for in the IEEE 802.3 networking standard. The local receiver status signal depends on a SNR as determined by the signal processing circuitry and on the state of a  
35 descrambler circuit included in the signal processing circuitry.

1 39499/FLC/B600-BP1426

If the SNR is low and the descrambler circuit can no longer  
decode the received signals, the signal processing logic sets the  
5 local receiver status signal to indicate that data can no longer  
be sent reliably over the network channel.

The communication modules further include control logic for  
generating a MSE signal 82 proportional to the Mean Square Error  
(MSE) produced by the signal processing circuitry. The MSE  
10 signal correlates with the SNR of the signal processing  
circuitry.

The s communication modules further include control logic  
for generating a receive error signal 84. The receive error  
signal is a combination of a false carrier sense signal as  
15 provided for in the IEEE 802.3 networking standard and a receive  
coding error as provided for in the IEEE 802.3 networking  
standard. The false carrier sense signal indicates if the local  
transceiver receives a frame that does not conform to the IEEE  
802.3 networking standard. The receive coding error signal  
20 indicates if a frame contains a packet with an error such as a  
premature packet end as provided for in the IEEE 802.3 networking  
standard.

The quality indicator logic receives a clocking signal 72  
from the local network chip's timing circuit. The clocking  
25 signal is used to provide timing information for the quality  
indicator logic module to generate a plurality of link quality  
indicator signals with various timing characteristics.

The quality indicator logic module receives the previously  
described network channel status signals and generates a single  
30 link quality indicator signal including a plurality of possible  
timing sequences 70. Each of the timing sequences encodes a  
different aspect of the quality of the network channel.

If the quality indicator logic module determines that the  
network link is functioning normally, the link quality indicator  
35 signal is driven low and remains low as long as the network link

1 39499/FLC/B600-BP1426

is functioning normally 86. This creates a first aperiodic link  
quality indicator signal indicating that the network link is  
5 fully operational.

If the local network chip is unable to establish and  
maintain a network channel with the remote network device, then  
the link quality indicator signal is driven high 88 and it  
remains in that state until the local network chip can  
10 reestablish the network channel. This creates a second aperiodic  
link quality indicator signal indicating that the network link  
is not operational at all.

If the local network chip detects a false carrier sense  
signal or a receive coding error signal, the link quality  
15 indicator signal is driven high then low at a low frequency 92.  
This creates a first periodic link quality indicator signal  
indicating that the network link is marginally operational  
because there are framing or receive coding errors.

If the local network chip detects that the MSE is greater  
20 than the SNR threshold value, then the link quality indicator  
signal is driven high then low at a high frequency 92. This  
creates a second periodic link quality indicator signal  
indicating that the network link is marginally operational  
because the MSE is higher than the SNR threshold value.

25 FIG. 2 is a block diagram depicting a embodiment of a  
quality indicator employing a Light Emitting Diode (LED) to  
generate a visual quality indicator according to the present  
invention. A quality indicator LED 200 is operably coupled to  
a previously described link quality indicator signal 40 and a  
30 voltage source 202. The link quality indicator signal is driven  
low and the LED is energized as soon as auto-negotiation is  
complete and the local network chip 10 is attempting to establish  
a network channel 32 (FIG. 1). After a network channel is  
established, the quality indicator LED will remain energized  
35 while the network channel is operating reliably with a good SNR.



1 39499/FLC/B600-BP1426

5 The link quality indicator signal will be driven high and the quality indicator LED will no longer be energized when the local network chip receive error signal indicates a problem with the communication channel and the local network chip is unable to receive packet data. The quality indicator LED will blink with a varying frequency to indicate intermediate levels of reliability of the network channel.

10 The following table summarizes the operation of the quality indicator LED:

LINK QUALITY	QUALITY INDICATOR LED STATUS
Auto-negotiation in progress	OFF
Auto-negotiation complete, establishing network channel	ON
Network channel established, high SNR	ON
Low SNR -close to data error	Fast blink
Receive bit errors detected	Slow blink
Local receiver failure	OFF

25 FIG. 3 is a process flow diagram of quality indicator process of an embodiment of a performance indicator according to the present invention. A quality indicator logic module 60 (FIG. 1) includes an encoded quality indicator process 62 for receiving a plurality of network channel status signals 53 (FIG. 1) transmitted by a local transceiver 50 (FIG. 1) and processing the plurality of received network channel status signals to produce a single link quality indicator signal 40 (FIG. 2) used to drive a link link quality indicator LED 200 (FIG. 2).

30 At reset 300, the quality indicator process turns a link quality indicator LED 200 (FIG. 2) off 302. The quality indicator process checks a auto-negotiation complete status signal 74 (FIG. 1) and loops back to reset the link quality

1 39499/FLC/B600-BP1426

indicator LED to off if the auto-negotiation complete signal indicates that the auto-negotiation process has not been  
5 completed.

If the auto-negotiation complete status signal indicates that the auto-negotiation process is complete, then the quality indicator process turns the link quality indicator LED on 308.

The quality indicator process checks 310 a link status  
10 signal 76 (FIG. 1) and returns to check the auto-negotiation complete status signal 304 if the link status signal indicates that the communication channel is no longer established. If the auto-negotiation complete status signal indicates that the auto-negotiation process is not complete, the quality indicator  
15 process turns the link quality indicator LED off 302 and continues monitoring the auto-negotiation complete status signal as previously described waiting for the auto-negotiation complete status signal to indicate that the auto-negotiation process is complete.

20 If the quality indicator process confirms that the communication channel is open 310, the quality indicator process checks to see if a local receiver status 80 (FIG. 1) indicates that data can no longer be sent reliably over the network channel. If data can no longer be sent reliably over the network  
25 channel, the quality indicator process turns the link quality indicator LED off 314 and returns to monitoring the link status signal 310 as previously described.

If local receive status signal indicates that data is being reliably sent over the network channel, the quality indicator  
30 process turns the link quality indicator LED on 316 and checks a receive error status signal 84 (FIG. 1) to confirm that there are no framing or receive coding errors as provided for in the IEEE 802.3 networking standard.

If the receive error status signal indicates that there are  
35 no framing or receive coding errors, the quality indicator

1 39499/FLC/B600-BP1426

process turns the link quality indicator LED off for a first period of time 320 and then back on 322 for the first period of time. The quality indicator process continues processing by returning to checking the link status signal 310.

In one embodiment of a quality indicator process according to the present invention, the link quality indicator LED is turned OFF for a period of 80 msec and back on for a period of 80 msec in order to cause the link quality indicator LED to blink at a low frequency if the auto-negotiation is complete and the network link is established but there are framing or receive coding errors.

In another embodiment of a quality indicator process according to the present invention, the on and off periods of the LED are different creating a periodic signal with asymmetric on and off periods.

If the receive error signal indicates that there are no framing or receive coding errors as provided for in the IEEE 802.3 networking standard, the quality indicator process checks a previously described MSE signal 82 (FIG. 1) by comparing 324 the MSE signal to a previously described SNR threshold signal 78 (FIG. 1). If the MSE signal is less than or equal to the SNR threshold signal, the quality indicator process continues processing by checking 310 the link status signal as previously described.

If the MSE signal is greater than the SNR threshold signal, the quality indicator process turns the link quality indicator LED off for a second period of time 326 and then back on 327 for the same period of time. The quality indicator process continues processing by returning to checking the link status signal 310.

In one embodiment of a quality indicator process according to the present invention, the link quality indicator LED is turned OFF for a period of 3 msec and back on for a period of 3

1 39499/FLC/B600-BP1426

msec in order to cause the link quality indicator LED to blink at a high frequency.

5 In another embodiment of a quality indicator process according to the present invention, the on and off periods of the LED are different creating a periodic signal with asymmetric on and off periods.

10 Although this invention has been described in certain specific embodiments, many additional modifications and variations would be apparent to those skilled in the art. It is therefore to be understood that this invention may be practiced otherwise than as specifically described. Thus, the present  
15 embodiments of the invention should be considered in all respects as illustrative and not restrictive, the scope of the invention to be determined by claims supported by this application and the claim's equivalents rather than the foregoing description.

20

25

30

35